

Figure 1

```

graph TD
    Processor[Processor 238] <--> IOPort[I/O Data Ports 246]
    Processor <--> Memory[Memory 236]
    Processor <--> Display[Display 234]
    Processor <--> Input[Input Devices 232]
    Processor <--> Speaker[Speaker 244]
  
```

Data Processing System
230

Figure 2

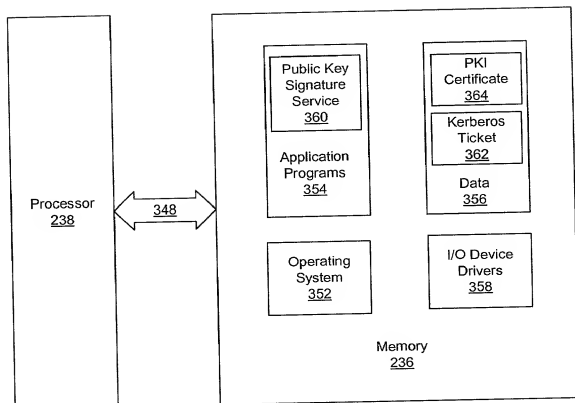


Figure 3

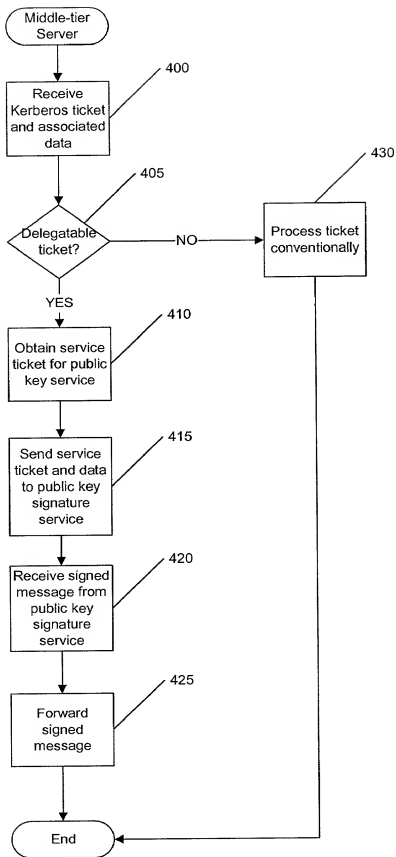


Figure 4

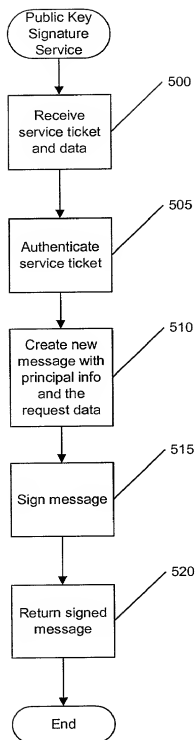


Figure 5A

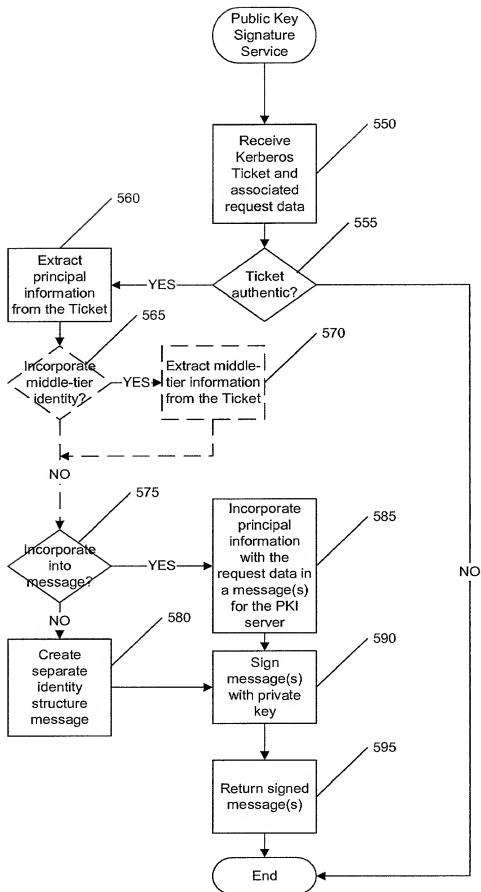


Figure 5B

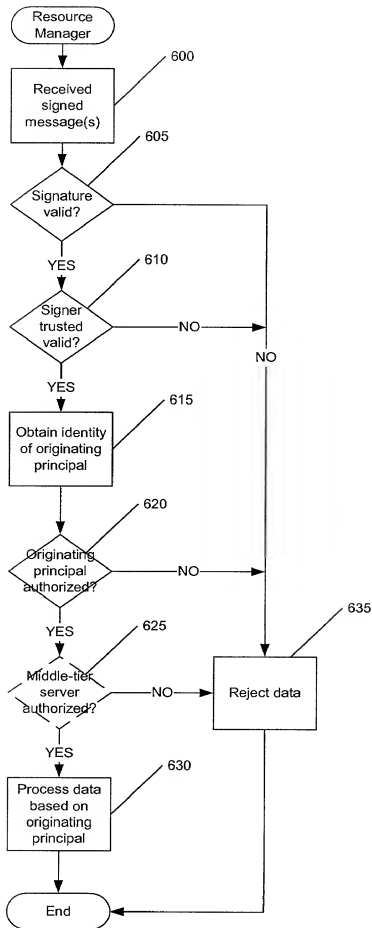


Figure 6